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** CONTINUING DATA ***** *JNA*

** FOREIGN APPLICATIONS ***** *JNA*

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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>JNA</i> Examiner's Signature _____ Initials _____	STATE OR COUNTRY TAIWAN	SHEETS DRAWING 5	TOTAL CLAIMS 25	INDEPENDENT CLAIMS 3
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TITLE
 System and method for identifying semiconductor process steps for queue-time control and abnormality detection

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